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From: Robert D. Marshall, Jr.  
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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of

TI-22423

Venkat V. Easwar

Art Unit: 2624

Serial No.: 09/713,446

Examiner: Kyle M. Pendergrass


Filed: November 15, 2000

Conf. No.: 3256

For: Multi-Level Dither Screening on a Split Arithmetic Logic Unit Processor

**CERTIFICATION OF FACSIMILE TRANSMISSION**

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 703-872-9306 on the date shown below:

  
Robin E. Barnum

October 15, 2004  
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<input type="checkbox"/> <b>DIVISIONAL APPN</b>	
<b>NAME OF INVENTOR(S):</b> Venkat V. Easwar	
<b>TITLE OF INVENTION:</b> Multi-Level Dither Screening on a Split Arithmetic Logic Unit Processor	
<b>TI FILE NO.:</b> TI-22423	<b>DEPOSIT ACCT. NO.:</b> 20-0668
<b>FAXED:</b> 10/15/04 <b>DUE:</b> 10/16/04 <b>ATTY/SECY:</b> RDM/reb	<b>RECEIPT DATE &amp; SERIAL NO.:</b> <b>Serial No.:</b> 09/713,446 <b>Filing Date:</b> November 15, 2000

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OCT 15 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Easwar

Art Unit: 2624

Serial No.: 09/713,446

Examiner: Kyle M. Pendergrass

Filed: November 15, 2000

Docket: TI-22423

For: MULTI-LEVEL DITHER SCREENING ON A SPLIT ARITHMETIC LOGIC UNIT  
PROCESSOR

AMENDMENT UNDER 37 C.F.R. 1.111

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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UNDER 37 C.F.R. §1.6(b)

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Robin E. Barnum

Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated July 16, 2004. These amendments are respectfully submitted as a full and complete response to that Action.

Please amend the above referenced application as follows: